

Form PTO-1449 (Modified)

# LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Atty Docket No.

F0522

Serial No.

09/879,724

Applicant:

Dong-Hyuk Ju et al.

Filing Date

June 12, 2001

Group

2826

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Sub-class	Filing Date if Appropriate
AS	5,877,046	03/02/99	Yu et al.	438	151	

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Sub-class	Translation	
						Yes	No
AS	06310427	04/11/94	JP			Partial	
AS	1100127 A1	16.05.2001	EP			X	

## OTHER ART

Examiner Initial	Author, Title, Date, Pertinent Pages, etc.
AS	Furukawa et al., "APPLICATIONS OF THE SILICON WAFER DIRECT-BONDING TECHNIQUE TO ELECTRON DEVICES", Applied Surface Science, Amsterdam, NL, vol. 41/42, 1989, pages 627-632, XP000431439.
AS	Kobayashi et al., "AN INTELLIGENT POWER DEVICE USING POLY-Si SANDWICHED WAFER BONDING TECHNIQUE", Proceedings of 1995 International Symposium on Power Semiconductor Devices & IC's, Yokohama, May 23, 1995, pages 58-62, XP000594241.
I	Fujino et al., "SILICON WAFER DIRECT BONDING THROUGH THE AMORPHOUS LAYER", Japanese Journal of Applied Physics, Tokyo, Japan, vol. 34, Part 2, No. 10B, October 1995, pages L 1322-1324.
✓	International Search Report for corresponding International Application PCT/US02/02991 dated 21/08/2002.

EXAMINER AS	DATE CONSIDERED 12-16-02
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

## Information Disclosure Statement PTO-1449 (Modified)

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